

VERSION WITH MARKINGS TO SHOW CHANGES MADE¹

In the Claims:

Claim 48 has been amended as follows:

48. (Amended) An energy-conserving microprocessor or CPU comprising:
- (a) keep-alive circuitry operable for performing auxiliary information processing when receiving keep-alive power; and
 - (b) main circuitry operable for performing main information processing only when receiving main power.

¹ Brackets “[]” indicate deletions and underlining “ ” indicates insertions.